

FIG. 1
(PRIOR ART)

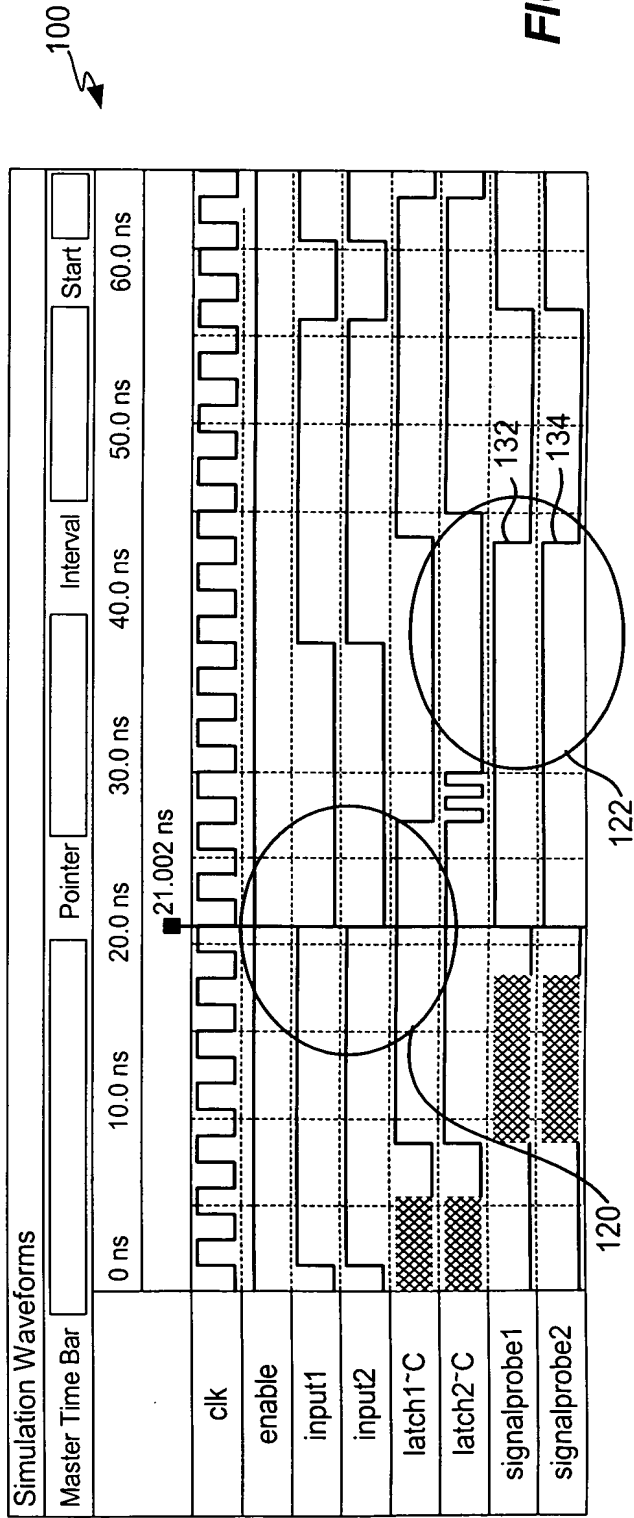
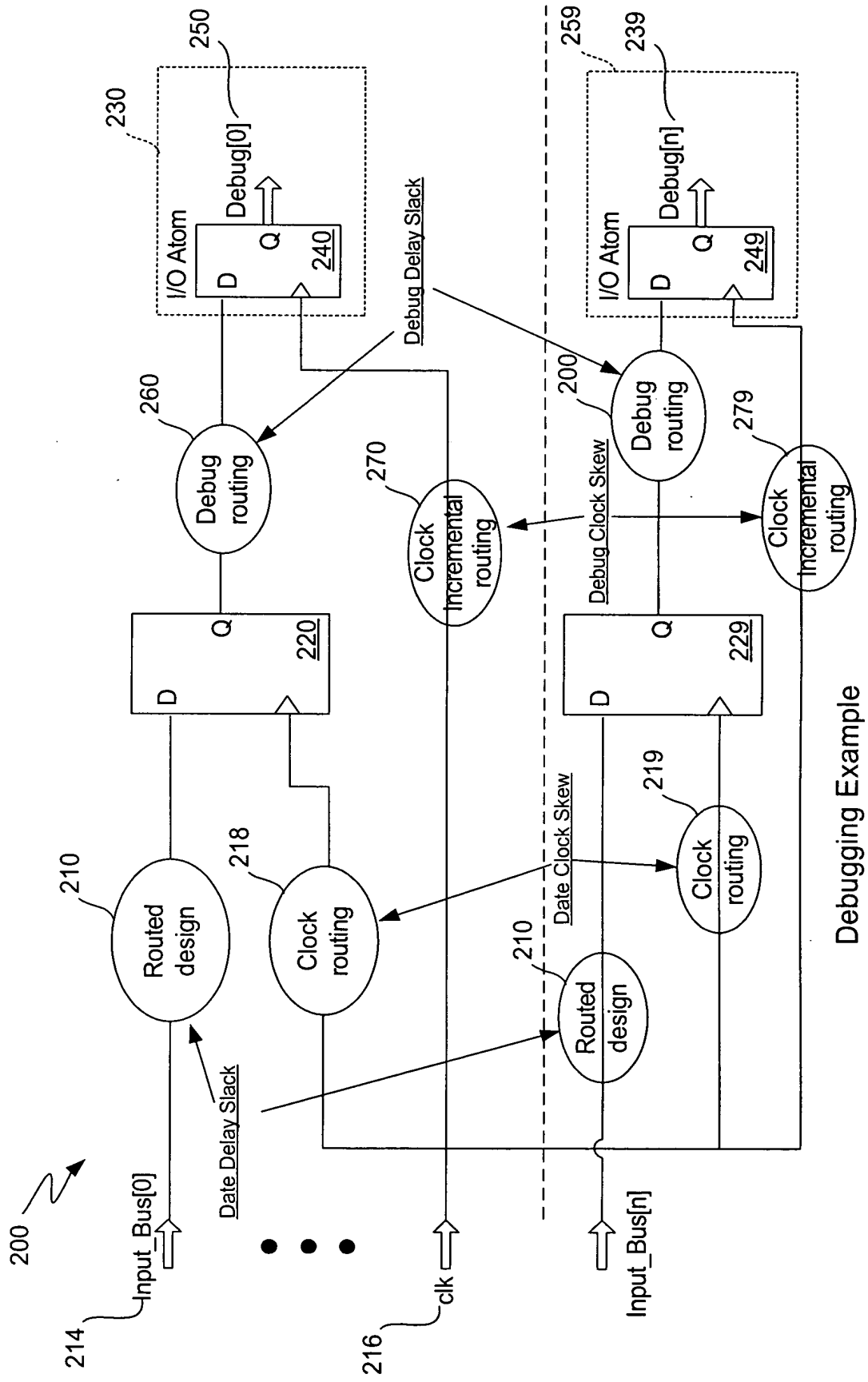


FIG. 2



Debugging Example

FIG. 3

User Interface Example

Assignment Editor

☒ SERDES transmitter
Custom region
Signal Probe
Timing
Multicycle
tco

☒ Information
This cell specifies the pin name to which you want to make an assignment.

	312	320	321	322	324	326	328
	Name:	Location	I/O Standard	Source Name:	Enabled:	Registers	Clock
1	? SignalProbe	Pin_AA7	LVTTL				
2	<<new>>	Pin_AA7	I/O Bank 7	Column I/O	DQ528~336		
3		Pin_AA8	I/O Bank 7	Column I/O	DQ387		
		Pin_AA12	I/O Bank 11	Column I/O	PLL6_OUT1p		
		Pin_AA13	I/O Bank 8	Column I/O	PLL6_OUT0p		
		Pin_AA15	I/O Bank 8	Column I/O			
		Pin_AA16	I/O Bank 8	Column I/O			
		Pin_AA17	I/O Bank 8	Column I/O			
		Pin_AA18	I/O Bank 8	Column I/O			
		Pin_AA19	I/O Bank 8	Column I/O			
		Pin_AA20	I/O Bank 8	Column I/O			

302

304

300

330

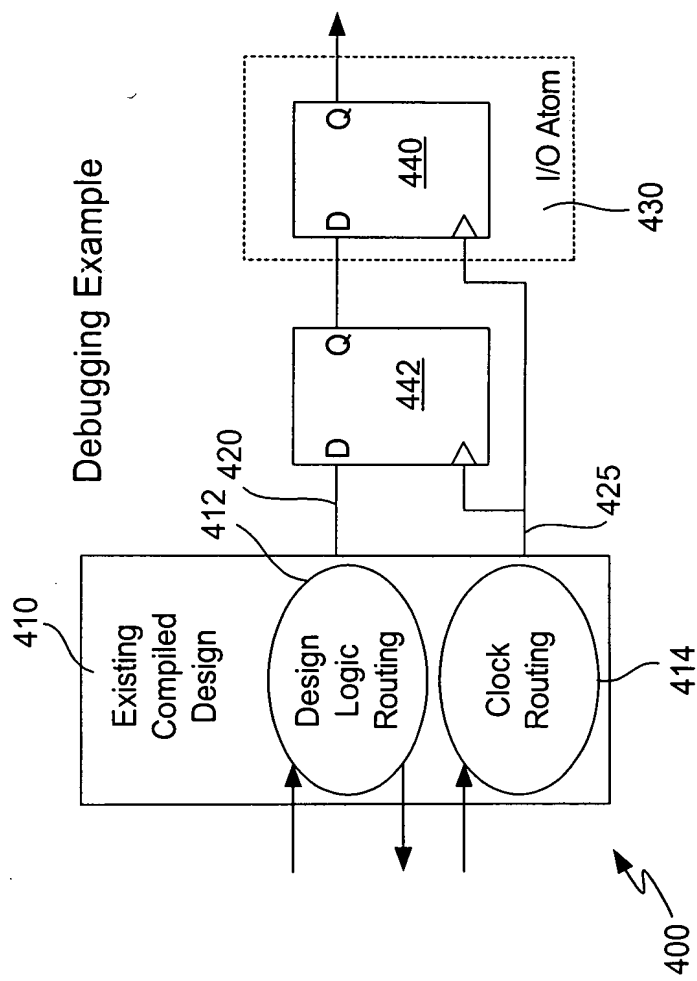
332

334

306

Registers	Clock	Clock	Node Finder
2	clk		

FIG. 4



Debugging Example

FIG. 5

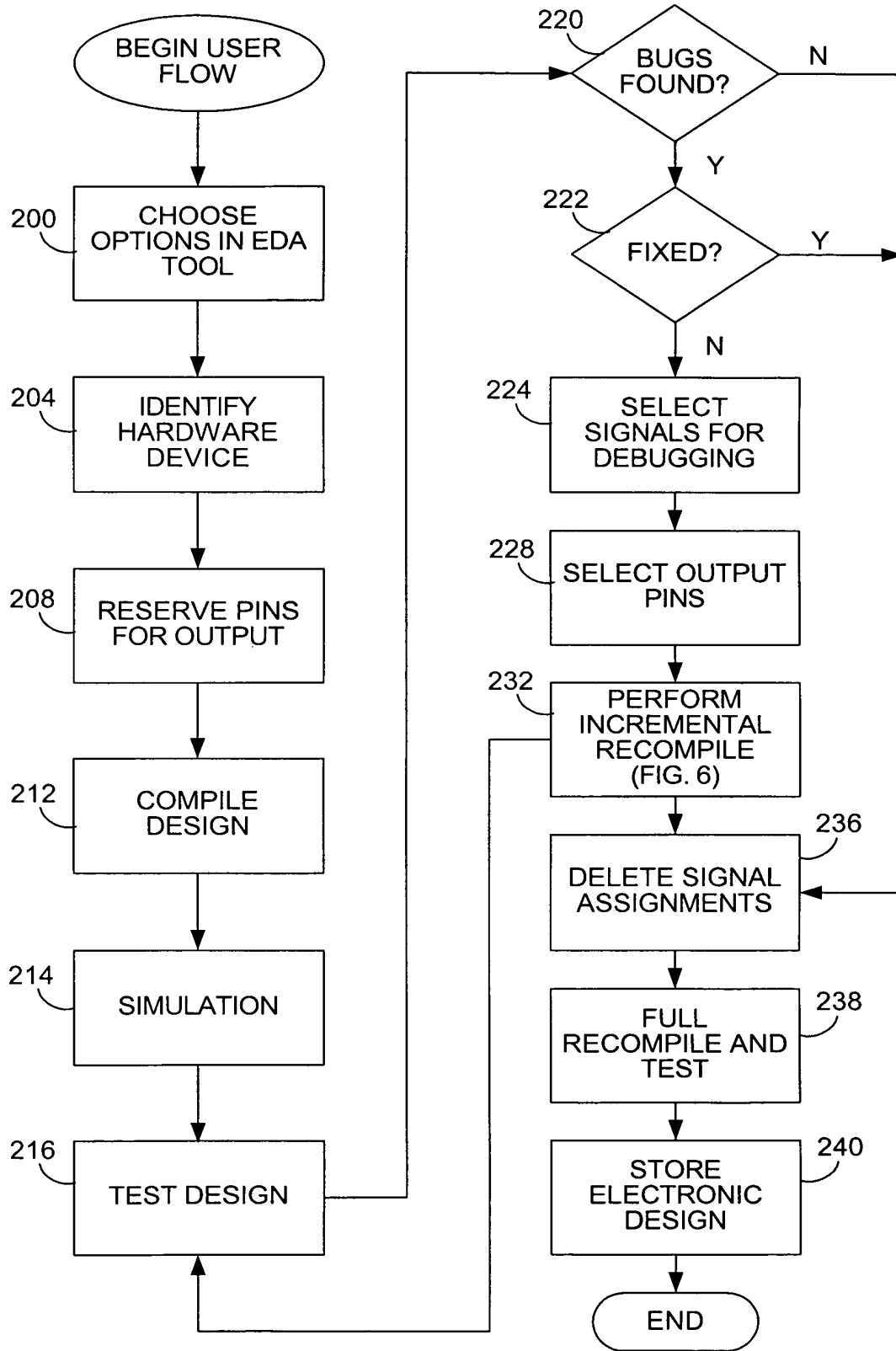


FIG. 6

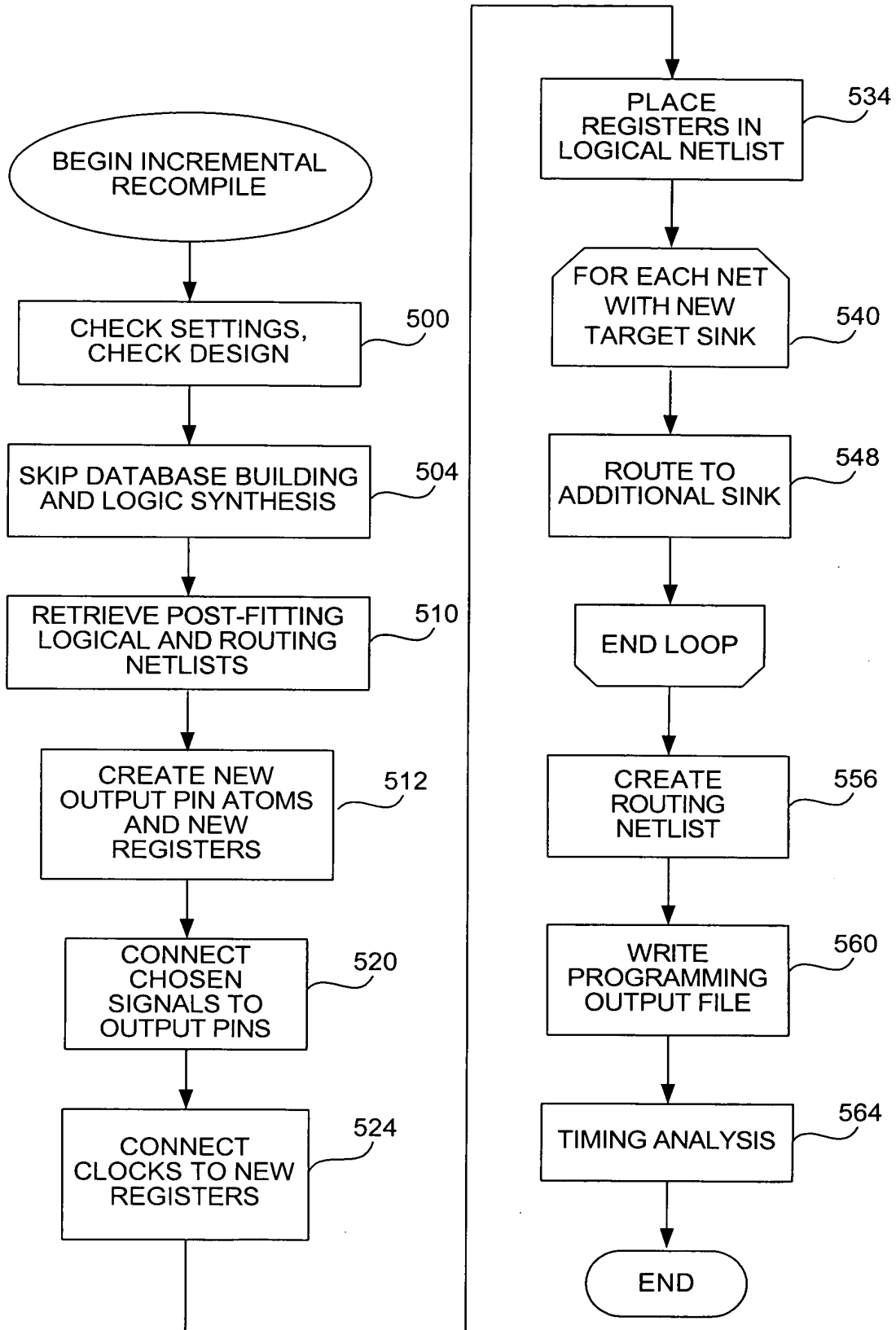
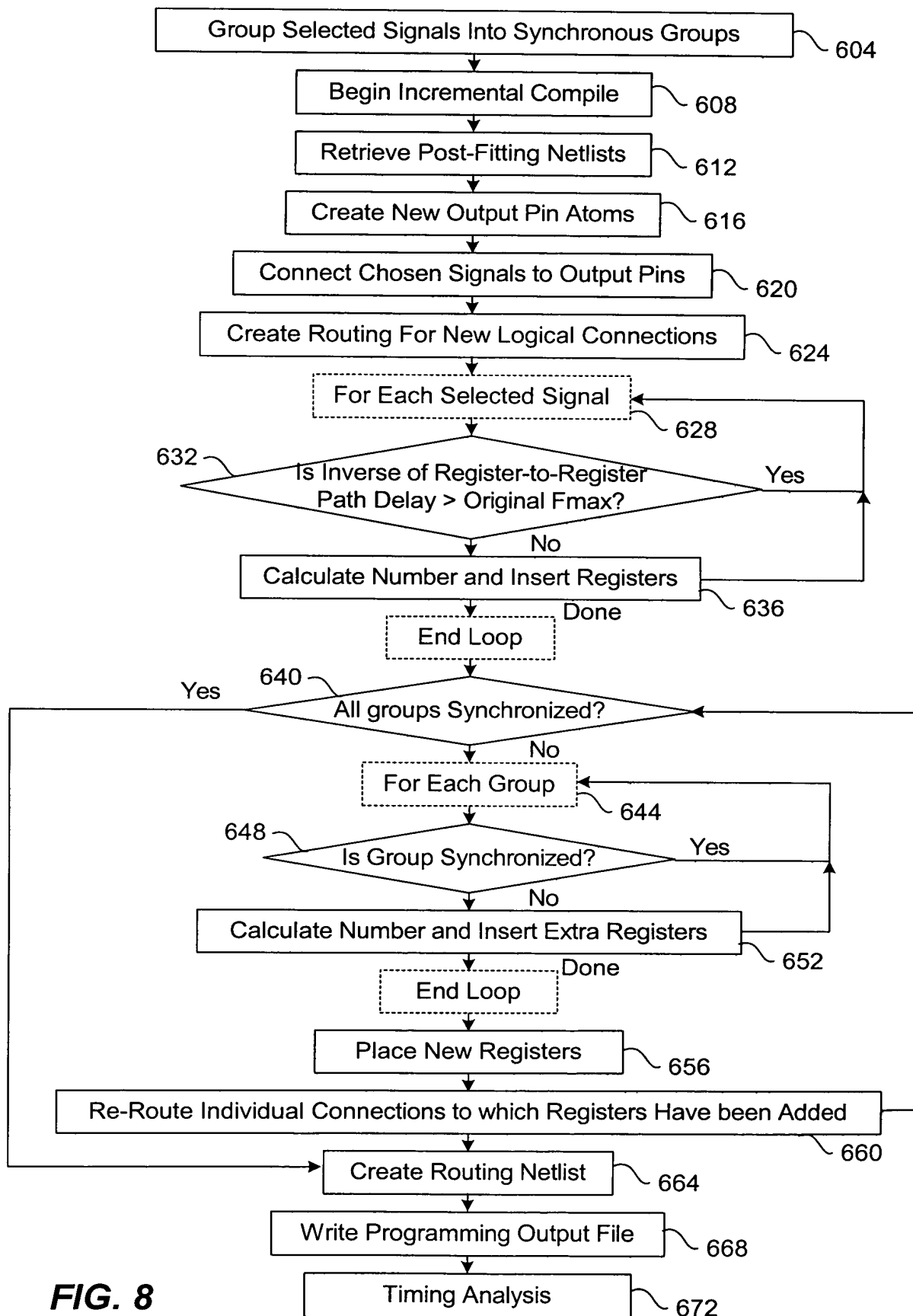


FIG. 7



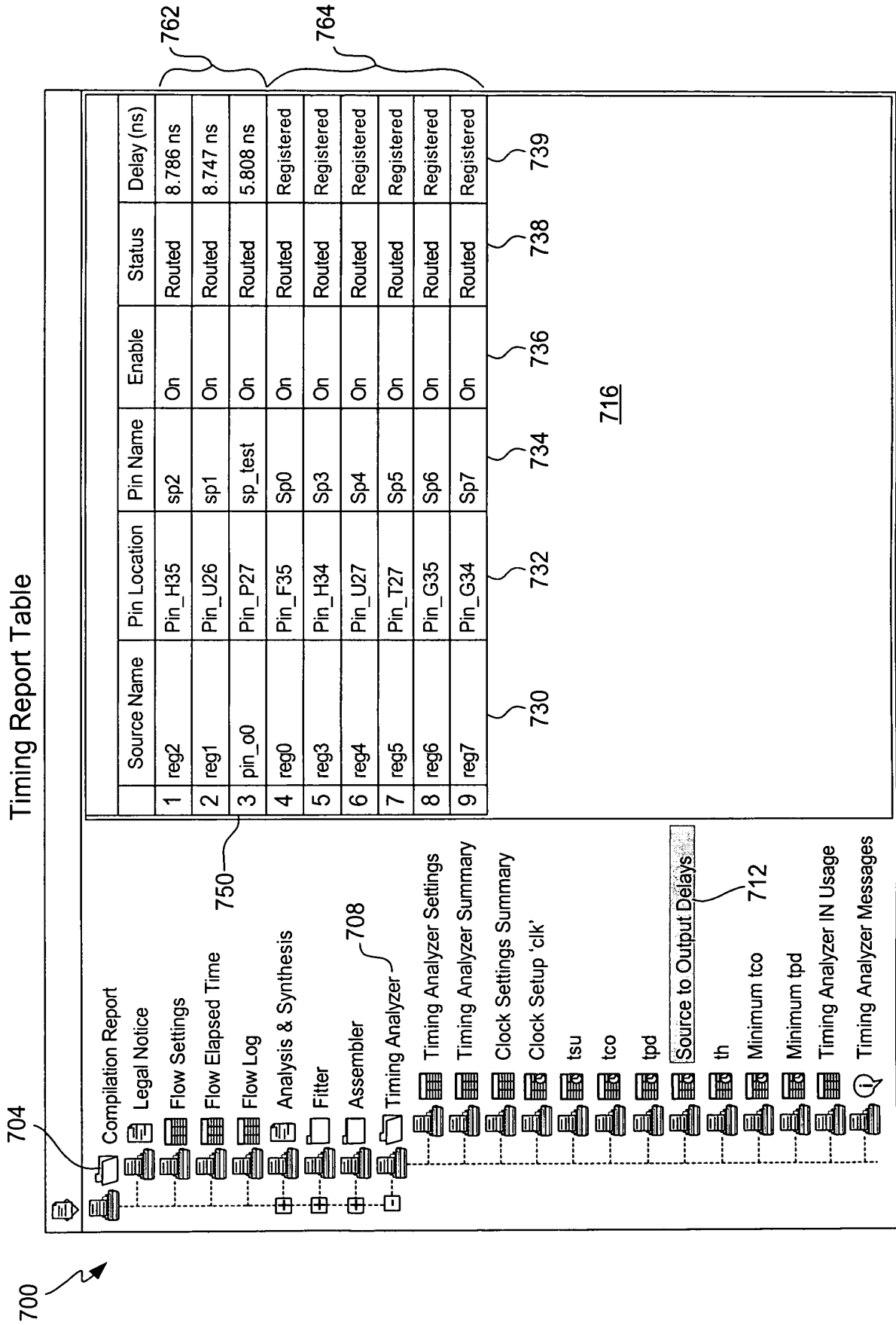


FIG. 9

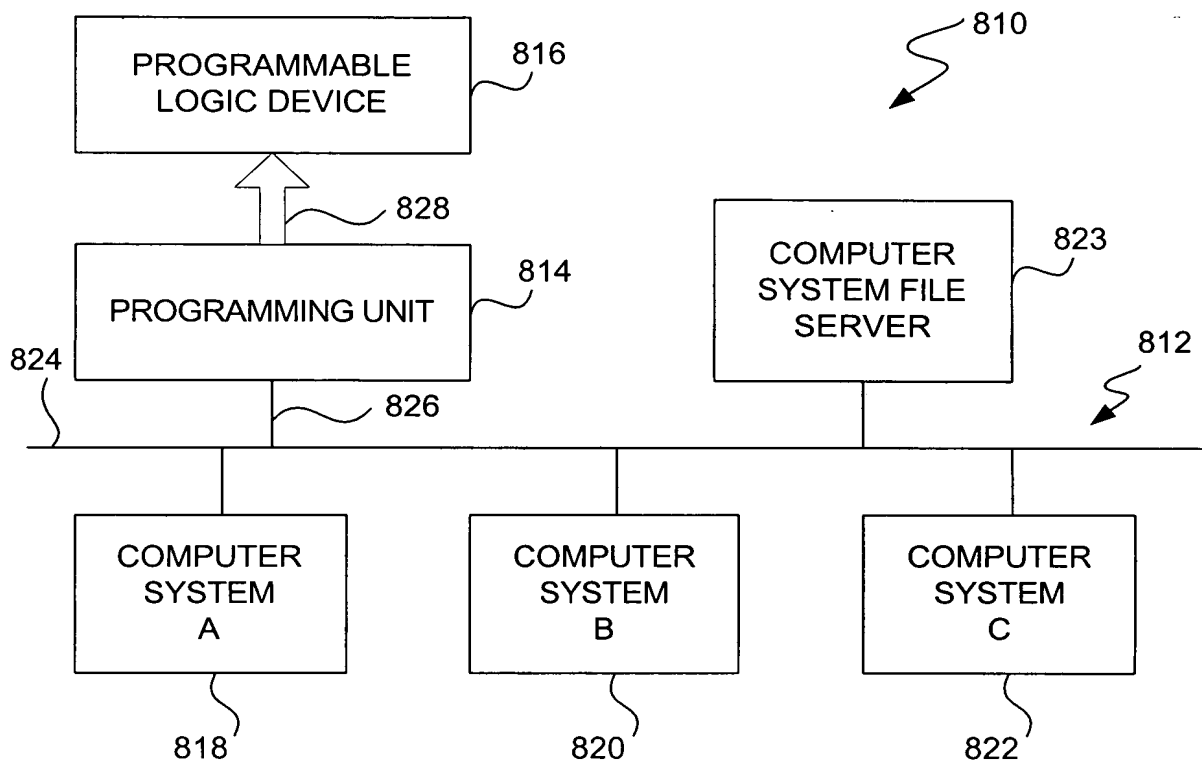


FIG. 10

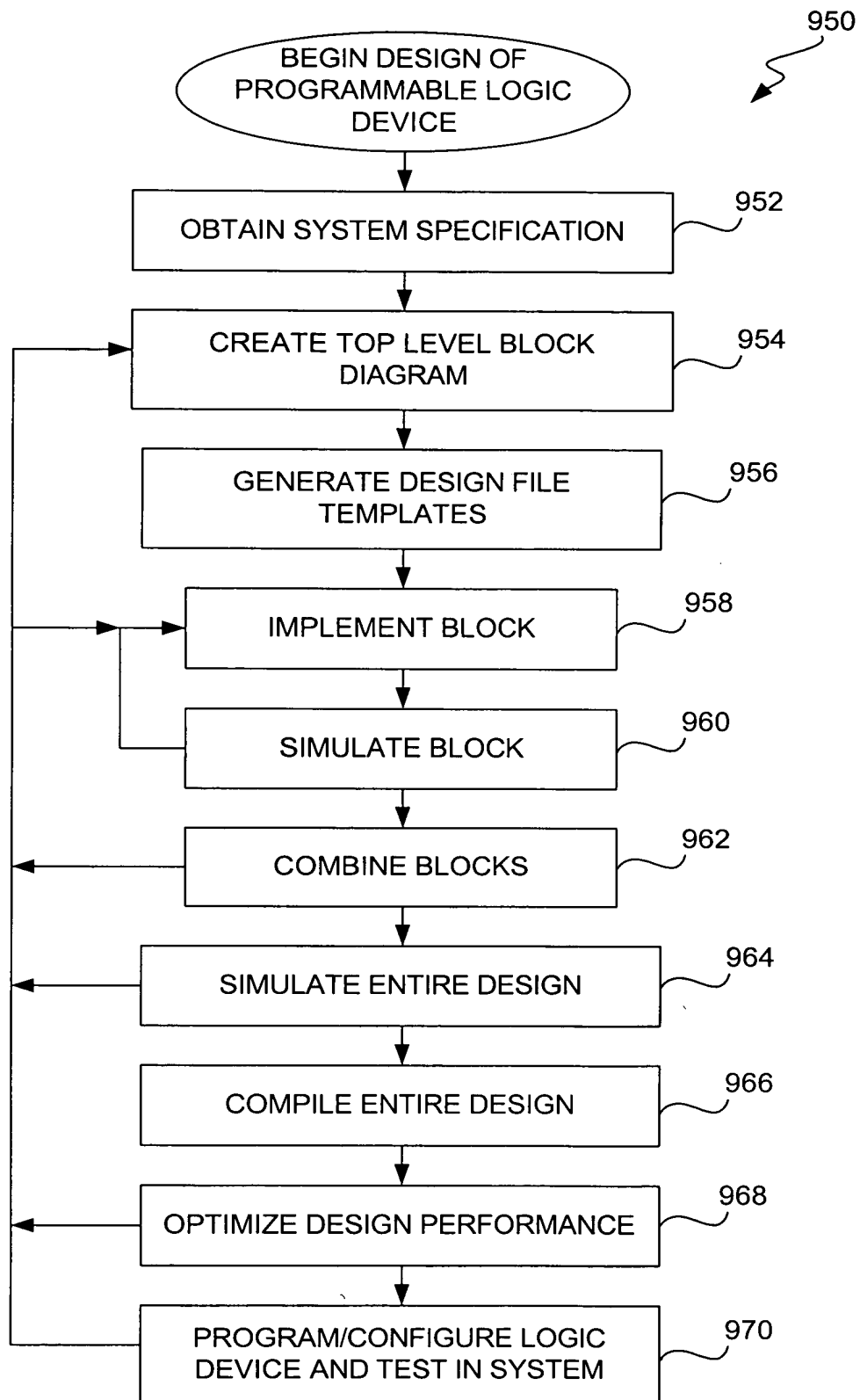


FIG. 11

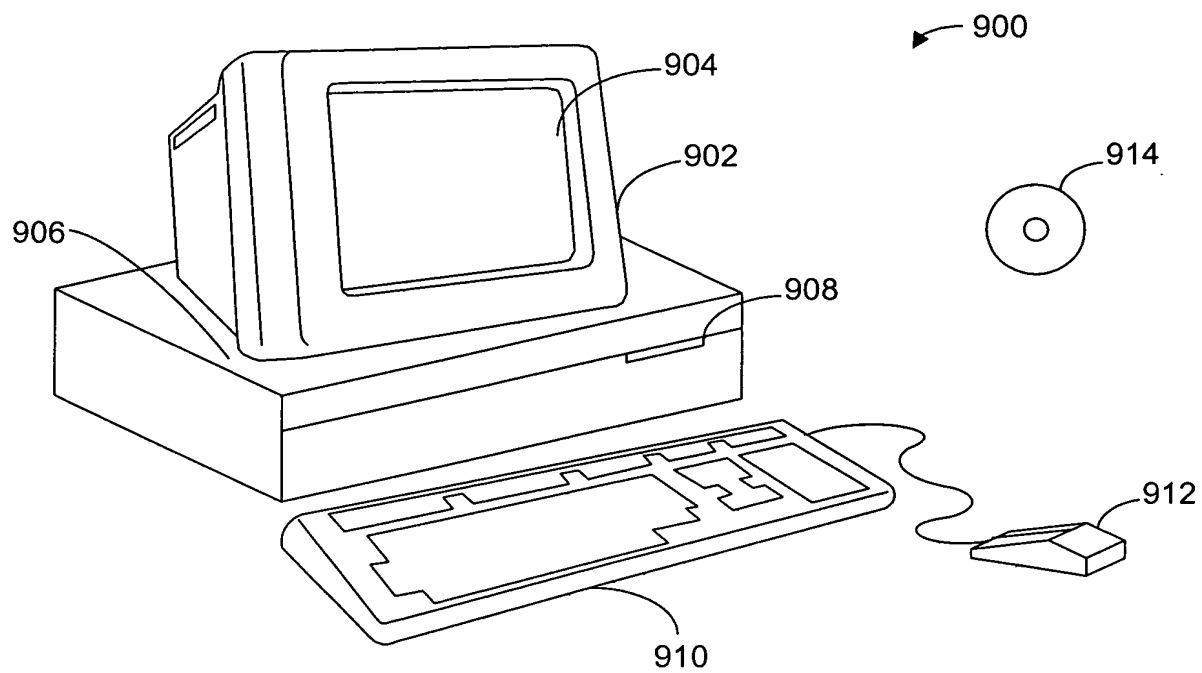


FIG. 12A

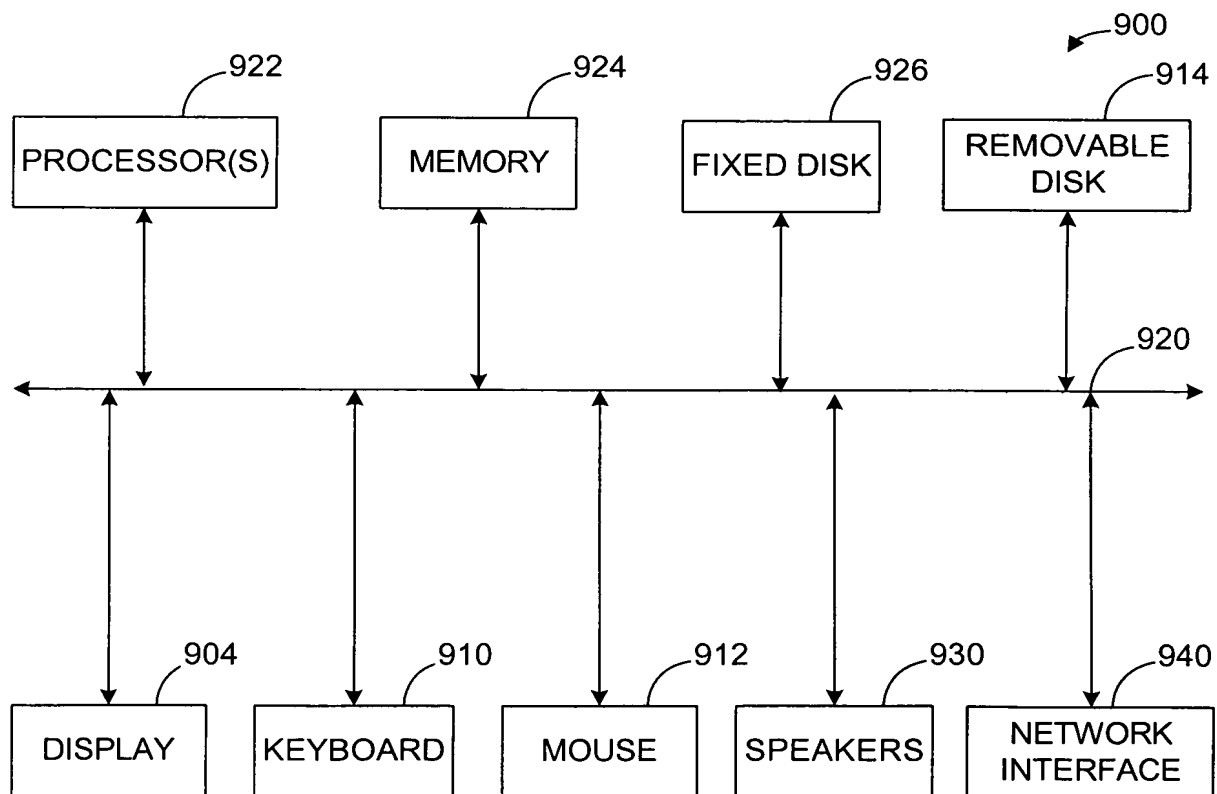


FIG. 12B